Quarter-Pixel Accuracy Motion Estimation (ME) - A Novel ME Technique in HEVC

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Abstract In this paper, we have focused on the development and implementation of novel low computational cost motion estimation (ME) algorithm for video coding based on H.265/HEVC standard. Through this algorithm, new ideas are explored for potentially improving the standard. High-Efficiency Video Coding (HEVC) is a new video compression standard currently being standardized by the JCTVC (Joint Collaborative Team on Video Coding) established by ISO/IEC MPEG and ITU-T. HEVC targets 50% coding gain over AVC/H.264 High Profile. In order to achieve this goal, new tools are being adopted to HEVC. One of the main differences of HEVC from previous video compression standards is the coding tree structure. In this structure, a frame is first divided into CTUs (coding tree units). Then a CTU is further divided into CUs (coding units) in a quad-tree structure. Unless a CU is further divided into four smaller CUs, it is predicted with one of several PU (prediction unit) types. Currently, CTU size can be as large as 64x64 and SCU (smallest CU) can be as small as 8x8. At the encoder side, decisions can be made to support only a subset of these PU types and CU sizes. This will present a trade-off between hardware complexity and coding efficiency. Since motion estimation is one of the most critical blocks in video encoders, trade-offs in motion estimation (ME) block are very important for the overall encoder design. However, it is important to consider the memory cost (in terms of area and data bandwidth) of these tools in hardware. This paper presents HEVC ME using Quarter –pixel accuracy prediction using 8-tap and 7-tap filter for luma interpolation using 2D Logarithmic search and goal is to reduce number of motion vector (MC) to increase compression gain, PSNR and to reduce memory area and data bandwidth bit rate.

Keywords: ME, HEVC, AVC, MC, MV, coding block, coding unit, SAO


1. Introduction

Recent advances in digital technologies have paved the way to the development of numerous real-time applications deemed too complex in the past. Digital video has been a regular presence in our lives for many years now. Whether used for digital television, in personal computers, hand held devices or other multimedia applications, its use has grown tremendously in the last years and it seems that this growth is not slowing down. A vast array of those applications requires transmission and storage of digital videos. Examples include but are not limited to: digital TV, video streaming, multimedia communications, remote monitoring, videophones and video conferencing. Advances in digital video can be classified as one of the most influential modern technologies; this is due to the fast wide spread use of digital video applications into everyday life. Consequently, over the last three decades, high-quality digital video has been the goal of companies, researchers and standardisation bodies [1,9].

The main idea behind video compression is to remove redundancies from the signals. This is carried out in the spatial domain within individual frames and in the temporal domain between neighbouring frames. Due to the fact video frames are typically displayed at a frame rate of 20 to 33 frames per second to the user, it is easy to understand that neighbouring frames often show high resemblance, hence removing the temporal redundancy can accomplish high compression ratios in practice. This is normally achieved in two steps: first, a Motion Estimation (ME) technique is used to calculate the motion distance, where blocks are defined areas within the frame. Secondly, with the available motion information, the residual between the current encoded frame and the previous frame is compensated and what is called Motion Compensation (MC) [9].

Motion estimation (ME) and motion compensation is seen as one of the most important methods of exploiting redundancy in motion pictures. Its importance is so high that 50% to 70% of encoder complexity is dedicated to the motion estimation process. However, as we move towards higher resolution videos, computational complexity is becoming a bigger concern [2]. This is why motion
estimation is seen as a major savings area in terms of computational expense. An important technique that can be used in ME is the Fractional Motion Estimation (FME). The FME allows an even greater efficiency by applying an interpolation process between integer position samples in reference frames, allowing a search for better matches in fractional positions. The FME is composed by two units: the Interpolation Unit, that generates the fractional position samples (sub-pixels), and the Search Unit, which searches for better matches composed by sub-pixels. One of the new FME is “Quarter-pixel accuracy motion estimation”, which is used for the most efficient video coding standards H.264/MPEG-4(AVC) and H.265/HEVC [4,5].

2. Motion Estimation in HEVC

High-Efficiency Video Coding (HEVC) is a new video compression standard currently being standardized by the JCTVC (Joint Collaborative Team on Video Coding) established by ISO/IEO MPEG and ITU-T. HEVC targets 50% coding gain over AVC/H.264 High Profile [2]. In order to achieve this goal, new tools are being adopted to HEVC. The main goals of H.265/HEVC standardization effort have been to enhance compression performance and provide a “network-friendly” video representation. H.265 utilizes variable block sizes and quarter-pixel motion compensation with multiple reference frames to achieve high coding efficiency. It has motion compensation units in sizes of 8x8, 16x16, 32x32 and 64x64 where H.264/MPEG-4 AVC supports only 4x4 to 16x16 [6]. Such wide block choices improve coding efficiency at the cost of largely increased motion estimation time [3]. In H.265/HEVC encoding, the most computationally critical part is motion estimation [9].

H.265/HEVC also has quarter-pixel motion vector accuracy as another of its important feature, which requires interpolation of pictures by a factor of four, which is done by a 7-tap bilinear filter and a 8-tap DCT (Discrete Cosine Transform) based finite impulse response (FIR) filter [2]. This increased accuracy of motion vectors and the subsequent coding gain is significant. On the other hand, the filtering process and the extra quarter-pixel motion estimation search demands substantial amount of computation. The computational complexity becomes even worse with larger search ranges, bi-directional and/or when multiple reference frames are used. Such high computational complexity is often a bottleneck for real-time conversational applications.

2.1. Sub-Pixel Interpolation Based Motion Estimation

Sub-pixel interpolation is one of the most computationally intensive parts of High Efficiency Video Coding (HEVC) video encoder and decoder. The fractional sample interpolation for luma samples in HEVC [3] uses separable application of an eight-tap filter for the half-sample positions and a seven-tap filter for the quarter-sample positions. This is in contrast to the process used in H.264/MPEG-4 AVC [5], which applies a two-stage interpolation process by first generating the values of one or two neighbouring samples at half-sample positions using six-tap filtering, rounding the intermediate results, and then averaging two values at integer or half-sample positions. In H.264, 4x4 and 16x16 block sizes are used. However, in HEVC, prediction unit (PU) sizes can be from 4x4 to 64x64. Therefore, HEVC sub-pixel interpolation is more complex than H.264 sub-pixel interpolation [8].

HEVC instead uses a single consistent separable interpolation process for generating all fractional positions without intermediate rounding operations, which improves precision and simplifies the architecture of the fractional sample interpolation. The interpolation precision is also improved in HEVC by using longer filters, i.e., seven-tap or eight-tap filtering rather than the six tap filtering used in H.264/MPEG-4 AVC [2,10,11]. Using only seven taps rather than the eight used for half-sample positions was sufficient for the quarter-sample interpolation positions since the quarter-sample positions are relatively close to integer sample positions, so the most distant sample in an eight-tap interpolator would effectively be farther away than in the half sample case (where the relative distances of the integer-sample positions are symmetric).

\[ a_{0,j} = \left( \sum_{i=-3}^{3} A_{i,j} \cdot \text{filter}[i] \right) \gg (B - 8) \]  
\[ b_{0,j} = \left( \sum_{i=-4}^{4} A_{i,j} \cdot \text{filter}[i] \right) \gg (B - 8) \]  
\[ c_{0,j} = \left( \sum_{i=-2}^{2} A_{i,j} \cdot \text{filter}[i-i] \right) \gg (B - 8) \]  
\[ d_{0,0} = \left( \sum_{i=-3}^{3} A_{0,j} \cdot \text{filter}[i] \right) \gg (B - 8) \]
where the constant $B \geq 8$ is the bit depth of the reference samples (and typically $B = 8$ for most applications) and the filter coefficient values for luma is given in Table 1 [4,5]. In these formulae $\gg$ denotes an arithmetic right shift operation.

### Table 1. Filter coefficients for luma fractional sample interpolation in HEVC

<table>
<thead>
<tr>
<th>Index $i$</th>
<th>-3</th>
<th>-2</th>
<th>-1</th>
<th>0</th>
<th>1</th>
<th>2</th>
<th>3</th>
<th>4</th>
</tr>
</thead>
<tbody>
<tr>
<td>$h_{filter}[i]$</td>
<td>-1</td>
<td>4</td>
<td>-11</td>
<td>40</td>
<td>40</td>
<td>-11</td>
<td>4</td>
<td>1</td>
</tr>
<tr>
<td>$q_{filter}[i]$</td>
<td>-1</td>
<td>4</td>
<td>-10</td>
<td>58</td>
<td>17</td>
<td>-5</td>
<td>1</td>
<td></td>
</tr>
</tbody>
</table>

The samples labelled $e_{0,0}, i_{0,0}, p_{0,0}, f_{0,0}, j_{0,0}, q_{0,0}, g_{0,0}, k_{0,0}$ and $r_{0,0}$ can be derived by applying the corresponding filters to samples located at vertically adjacent $a_{0,j}$, $b_{0,j}$ and $c_{0,j}$ positions as follows:

\[
h_{0,0} = (\sum_{i=-3}^{3} a_{0,i} h_{filter}[i]) \gg (B-8) \quad (2.5)
\]

\[
n_{0,0} = (\sum_{i=-3}^{3} a_{0,i} q_{filter}[i-1]) \gg (B-8) \quad (2.6)
\]

2.3. Interpolation Process of Chrominance Sample

Figure 2 shows the positions of the integer pixel sample, 1/2 pixel sample, 1/4 pixel sample, 1/8 pixel sample of the chrominance components of the reference image. It is supposed that chrominance sample point $B_{i,j}$ is located at the integer sample point $(xB_{i,j}, yB_{i,j})$, then the predicted value from chrominance point ‘ab0,0’ to ‘hh0,0’ at non-integer sample positions can be obtained by the 4-beat filter with the coefficientient is given in Table 2 [5].

### Table 2. Filter coefficients for chroma sample interpolation in HEVC

<table>
<thead>
<tr>
<th>Index</th>
<th>-1</th>
<th>0</th>
<th>1</th>
<th>2</th>
</tr>
</thead>
<tbody>
<tr>
<td>filter1[i]</td>
<td>-2</td>
<td>58</td>
<td>10</td>
<td>-2</td>
</tr>
<tr>
<td>filter2[i]</td>
<td>-4</td>
<td>54</td>
<td>16</td>
<td>-2</td>
</tr>
<tr>
<td>filter3[i]</td>
<td>-6</td>
<td>46</td>
<td>28</td>
<td>-4</td>
</tr>
<tr>
<td>filter4[i]</td>
<td>-4</td>
<td>36</td>
<td>36</td>
<td>-4</td>
</tr>
</tbody>
</table>

The values of 1/2 pixel points $ac0,0, ae0,0$; 1/4 pixel points $ab0,0, ad0,0, ae0,0, af0,0, ah0,0, ba0,0, da0,0, fa0,0, ha0,0$ can be obtained by using filter interpolation mentioned in Table 2 on the nearest integer pixel in the horizontal and vertical directions and similarly the value of sub-pixel sample point $bX0,0, cX0,0, dX0,0, fX0,0, gX0,0, hX0,0$ (among which, $X$ presents any one in b, c, d, e, f, g and h) can be obtained by the 4-beat filter interpolation in the vertical direction.

### Figure 2. Positions of Integer Sample Point and Non-integer Sample Point in the Interpolation of Chrominance

3. Simulation Results & Analysis

3.1. Results of Quarter-pixel Motion Estimation
To illustrate the implementation result of quarter-pixel motion estimation in HEVC, experiment have been carried out using MATLAB. Motion vectors are obtained by using simple 2D Logarithmic search algorithm. The characteristics of the motion activities of the blocks in the current frame are predicted using this temporal information. As discussed, we implemented the Quarter-pixel interpolation algorithm with implementation of 2D Logarithmic search to find the motion vector, predicted frame with PSNR and residual with motion compensation and the corresponding 3D mesh plot of residual.

The experiment has been carried out by taking different CTU size i.e. 8X8, 16X16, 32X32, and 64X64 of different video frames such are AVI, DIVX and YUV. For 8x8, 16X16, 32X32, and 64X64 CTU size the search block size 10x10, 20x20, 40x40 and 70x70 respectively. ME results of only 8x8 CTU of AVI, 16x16 CTU of DIVX and 32x32 CTU of YUV video frames are shown in Figure 3, Figure 4, and Figure 5.

### 3.2. Results of ME by Taking 8x8 CTU of an AVI Video Frame

In order to find the motion vector, predicted frame with PSNR and with motion compensation and the corresponding 3D mesh plot of residual of an AVI video frames, here the CTU size is considered as 8x8 and the searching block size around CTU is 10x10.

![Reference Frame Candidate](image1)

![Candidate Frame, PSNR 27.2146dB](image2)

![Motion vector field between reference & candidate frame. 8X8 CTU](image3)

![Predicted Frame, PSNR 33.0892dB, Abs. Difference with MC, and 3D Mesh plot](image4)

*Figure 3. Results of ME by taking 8x8 CTU of AVI video Frames*
3.3. Results of ME by Taking 16x16 CTU of a DivX Video Frame

In order to find the motion vector, predicted frame with PSNR and with motion compensation and the corresponding 3D mesh plot of residual of a DIVX video frames, here the CTU size is considered as 16x16 and the searching block size around CTU is 20X20.

![Reference Frame](image1)

![Candidate Frame](image2)

![Motion vector field between reference & candidate frame. 16x16 CTU](image3)

3.4. Results of ME by Taking 32x32 CTU of a YUV Video Frame

In order to find the motion vector, predicted frame with PSNR and with motion compensation and the corresponding 3D mesh plot of residual of a YUV video frames, here the CTU size is considered as 32x32 and the searching block size around CTU is 40X40.
4. Result Analysis

The H.265 encoding method has been complicated by the development of new coding tools. Among those tools, the quarter pixel accuracy motion estimation and compensation enhance compression gain and to reduce memory area and data bit rate and it requires the implementation of complex interpolation filters and increases the ME complexity. Here the scheme was divided into four steps; in the first step the sub-pixel ME for the 8×8 and 16×16, 32×32 and 64×64 block has been used.

The result shows that with increase of size of CTU block, the PSNR of predicted frame gradually decreases and also the number of motion vector reduces as given in Table 3. The PSNR of original candidate frame was 27.2146 dB, 27.7167 dB and 28.7961 dB with respect to reference frame for AVI, DIVX and YUV video frames respectively.

<table>
<thead>
<tr>
<th>CTU size</th>
<th>AVI (PSNR(dB))</th>
<th>DIVX (PSNR(dB))</th>
<th>YUV (PSNR(dB))</th>
</tr>
</thead>
<tbody>
<tr>
<td>8x8</td>
<td>33.0892</td>
<td>34.2917</td>
<td>30.8412</td>
</tr>
<tr>
<td>16x16</td>
<td>31.6316</td>
<td>32.4501</td>
<td>29.9705</td>
</tr>
<tr>
<td>32x32</td>
<td>29.8129</td>
<td>30.1317</td>
<td>28.364</td>
</tr>
<tr>
<td>64x64</td>
<td>28.364</td>
<td>29.2345</td>
<td>28.4357</td>
</tr>
</tbody>
</table>

Table 3. PSNR of different Video Frames with different size CTU
5. Comparison with H.264/AVC

Quarter-pixel interpolation using 7-tap and 8-tap filter in HEVC gives more details in comparison to 6-tap filter base quarter-pixel interpolation in H.264/AVC. The computational cost of this process can be reduced utilizing parallel processing technique in the hardware implementation. By comparing the PSNR, obviously the PSNR of predicted frame produced by the use of H.264 Quarter-pixel interpolation filter is much less in comparison to Quarter-pixel Interpolation filter of HEVC. The performance of the quarter-pel filters in H.264/AVC is relatively poor, especially the filters for the quarter-pel pixels e, g, p and r in the diagonal direction. In general performance gain (more than 10%) of interpolation filters in HEVC compared to H.264/AVC comes from the quarter-pel interpolations.

6. Conclusion

Quarter-pixel interpolation based motion estimation is an optimized process and normally used to increase the compression gain in HEVC and which is here implemented in MATLAB. According to the experimental results, this implementation of quarter-pixel interpolation based motion estimation working as like as HEVC reference software HM 5.2 with the promotion of the next generation video coding standard HEVC. To enable a parallel processing, the macro blocks (CTU) are processed on dedicated and special processor, so the all motion vectors will be outputted for an individual frame at the same time. This technique may increases the requirement of hardware resources but definitely reduces the time and computation complexity and also increases compression gain of encoder.

References